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INTECH 3.0-097

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Peter Poechmueller

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: July 24, 2003

Examiner: Not Yet Assigned

For: ARRAY TRANSISTOR AMPLIFICATION

METHOD AND APPARATUS FOR DYNAMIC

RANDOM ACCESS MEMORY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of the reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

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Dated: July 24, 2003

Respectfully submitted,

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S.	TATEMENT	BY AP	PLICANT	First Named Inventor	Peter Poechmueller		
				Art Unit	N/A		
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Sheet	1	of	1	Attorney Docket Number	INTECH 3.0-097 03 P 50758 US		

U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where		
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	AA	US 2002/0196651-A1	12-26-2002	Rolf Weis			

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